

12.1.3 Generic system outline

During the even graphical display

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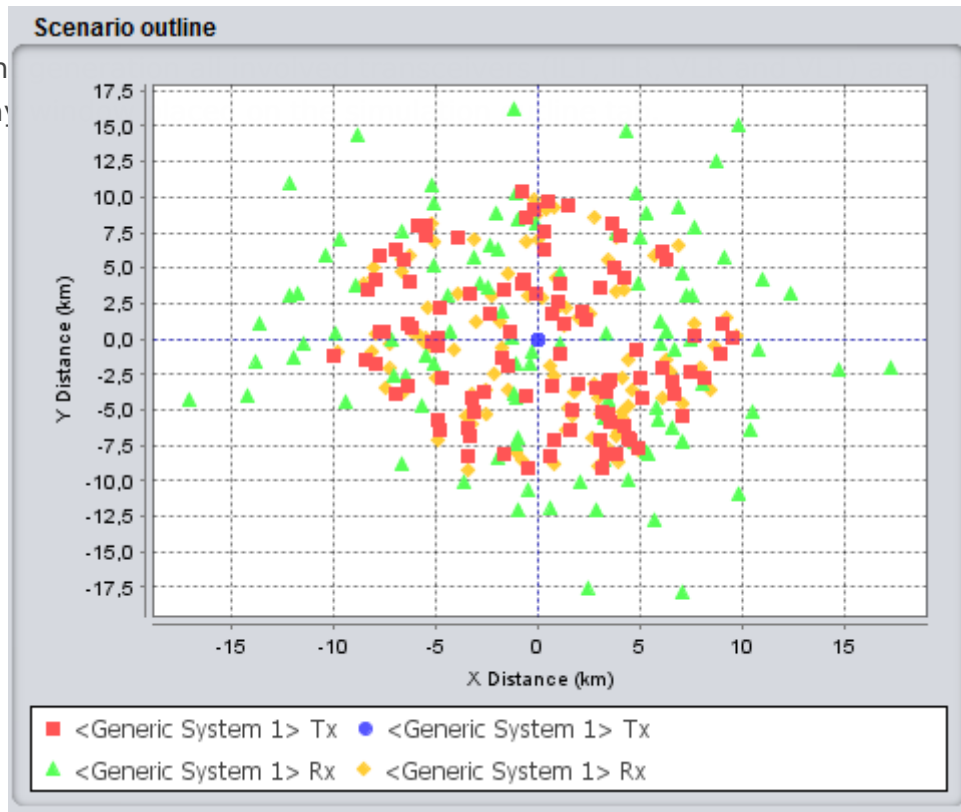


Figure 243: Example of “Scenario outline” for a generic system 1 vs generic system 2 type of simulation

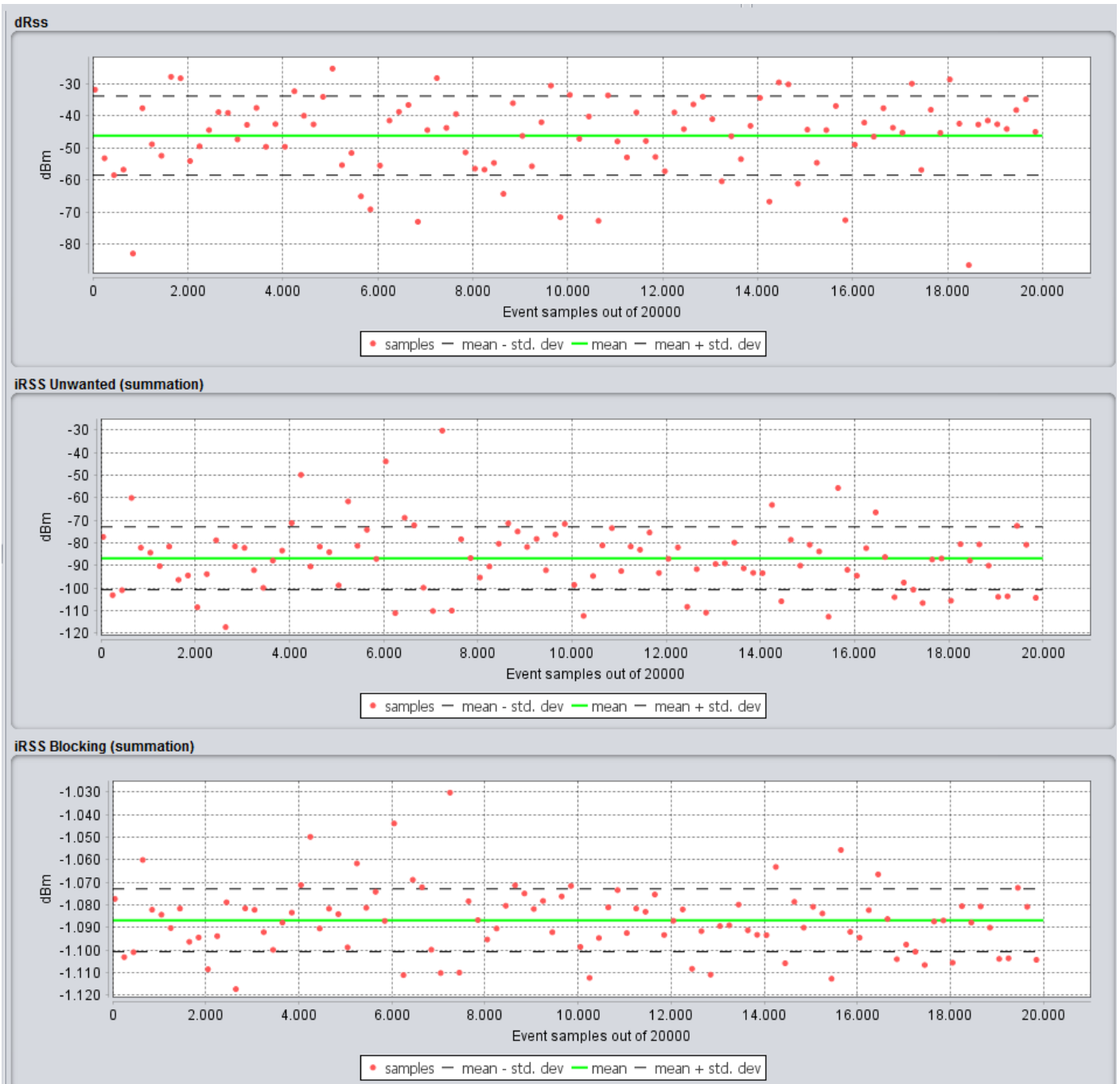


Figure 244: dRSS, iRSS samples

The dRSS and the iRSS vectors are shown for 100 events maximum to avoid overloading the memory and to speed the computation time. The iRSS is shown as a summation of all the interferers.

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